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FORM PTO-1449 (Modified

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

Page 1 of 4

(Use several sheets if necessary)



| ATTY. DOCKET NO. | SERIAL NO. |
|---|---|
| RPS920010128US1 | 10/016,448 |
| APPLICANT: R. T. Bailis, et al. | |
| FILING DATE: 12/10/2001 | GROUP: 2862 |



REFERENCE DESIGNATION

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| EXAMINER INITIALS | | DOCUMENT NUMBER | | | | | | DATE | NAME | CLASS | SUBCLASS | FILING DATE (IF APPRO.) |
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| CB | 6 | 1 | 7 | 3 | 4 | 1 | 9B1 | Jan. 9, 2001 | Barnett | 714 | 28 | May 14, 1998 |
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| CB | CC | R. T. Bailis et al., U.S. Pending Patent Application Serial No. 10/016772 (docket RPS920010126US1), "Method and System for Use of an Embedded Field Programmable Gate Array Interconnect for Flexible I/O Connectivity" |

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| C Britt | 6-10-04 |

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP.609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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| CB DD | R. T. Bailis et al., U.S. Pending Patent Application Serial No. 10/016449 (docket RPS920010127US1), "Method and System for Use of a Field Programmable Gate Array Function within an Application Specific Integrated Circuit (ASIC) to Enable Creation of a Debugger Client within the ASIC" |
| CB EE | R. T. Bailis et al., U.S. Pending Patent Application Serial No. 10/015922 (docket RPS920010129US1), "Method and System for Use of a Field Programmable Interconnect within an ASIC for Configuring the ASIC" |

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| LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT<br>Page 3 of 4 | RPS920010128US1 | 10/016,448 RECEIVED<br>APR 25 2002 |
| (Use several sheets if necessary) | APPLICANT:<br>R. T. Bailis, et al. | Technology Center 2100 |
| | FILING DATE:<br>12/10/2001 | GROUP: 2862 |

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